

Arnold O. Allen

Data Processing Division, Systems Science Institute, Los Angeles, California.

Mathematics (Ph. D., University of California, Los Angeles, 1962). He joined the IBM Federal Systems Division in 1956 as a programmer. Since receiving his Ph. D. degree through an IBM Fellowship, he has held positions in sales and systems engineering, and has worked in the field of applied mathematics at the IBM Scientific Center, Los Angeles. He is currently a Senior Staff Member at the Systems Science Institute, specializing in the areas of programmer productivity techniques, and in the application of statistics and queuing theory to communication system design.

Henderson Cole

Research Division, Thomas J. Watson Research Center, Yorktown Heights, New York.

Physics (Ph. D., Massachusetts Institute of Technology, 1952). Joined IBM Research in 1955 as head of the x-ray diffraction activity. In the early nineteen sixties, he automated several x-ray diffraction instruments with an IBM 1620 and later with an IBM 1800 computer system. Became head of the newly created laboratory automation activity in 1970. Currently working on a broader range of laboratory automation problems.

Hugh W. Lynch

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Mathematics (M. A., Boston University, 1960). Before joining IBM in 1964, he was a programmer with the MITRE Corporation. At IBM Research he managed the operations of a Computer-Aided Instruction (CAI) system. Then he set up and managed a group to analyze the performance and usage of the TSS/360, CP-67, and OS/MVT systems at the IBM Research Center. He is presently working on systems planning and technical services for the IBM marketing group.

J. Bartlett Page

System Development Division, Poughkeepsie, New York.

Mathematics (M. S., University of Rochester, 1961). Joined IBM in 1961 as a programmer. He has been involved with OS/360 development, and he is working on the design of OS/VS2 Release 2.

Keith D. Ryder

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Mathematics (M.A., University of Detroit, 1965). After joining IBM in 1965, he worked on control program design and development for the OS/360 MFT and MVT systems. Subsequently, he was involved in the definition of the address translation architecture for System/370 virtual systems. He then participated in the architecture, design, and performance analysis of OS/VS2 Release 1. Currently he is involved with the overall design of VTAM.

Authors